

DIGITAL SERVO MOTOR CONTROLLER IC DESIGN FOR PREVENTING THE POWER FEEDBACK EFFECT DURING MANUAL ADJUSTING THE SERVO MOTOR

5 FIELD OF THE INVENTION

The present invention relates to an improvement design of an IC, and more particularly to an improvement design of a digital servo motor controller IC for preventing the power feedback effect during manual adjusting the servo motor.

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BACKGROUND OF THE INVENTION

The system block diagram of a conventional remote-controlled game motor or plane is shown in Fig. 1, in which the remote-controlled signal transmitter 2 accepts manual instructions from the joystick 1, and transmits signals to the receiver 3 of a game motor or plane. After being processed the signals will be inputted to a digital servo motor controller 4 for controlling the motor 5 of the game motor or plane, so as to rotate the wheels of the motor or control the propellers of the plane.

20 The circuits between the digital servo motor controller 4 and the motor 5 are shown in Fig. 2. The digital servo motor controller 4 has two outputs OUT1 and OUT2, after being passed through the INV1 and INV2 respectively to generate OUT1B and OUT2B, four signals OUT1, OUT2, OUT1B, OUT2B will be inputted to power MOS transistors PMOS1, NMOS2, PMOS3, NMOS4 respectively. Four diodes D1, D2, D3, D4 are parallel connected with the four power MOS transistors PMOS1, NMOS2, PMOS3,

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NMOS4 respectively as shown. Since the breakdown voltage of the four diodes is lower than that of the four power MOS transistors, the four power MOS transistors will be prevented from damaging by any static voltage. PMOS1 and NMOS2 are serially
5 connected at point A. PMOS3 and NMOS4 are serially connected at point B. The motor 5 is connected between point A and B.

When OUT1 and OUT2 are both "0", the motor 5 is stopped. When OUT1 is "0", OUT2 is "1", the motor 5 will be turned left. When OUT1 is "1", OUT2 is "0", the motor 5 will be turned right.
10 OUT1 and OUT2 are both "1" is prohibited. Table 1 shows the results.

Table 1

OUT1	OUT2	motor 5
0	0	stop
0	1	turn left
1	0	turn right
1	1	prohibited

Table 1 shows the operation of the system. When the system
15 is not operated, the digital servo motor controller 4 and the motor 5 are not powered on, and the motor 5 can be rotated manually to adjust the initial position. As the motor 5 is rotated manually, the motor 5 will become a generator to generate some power to be fed back to digital servo motor controller 4 and cause both OUT1 and
20 OUT2 to be "1", so as to short the points A and B of the motor 5. Due to this short situation, a huge amount of reverse current will be generated and fed to the motor 5, and generate a great reverse

torque to resist the manual rotating of the motor, therefore the initial position of the motor 5 is not very easy to be adjusted.

OBJECT OF THE INVENTION

5 It is therefore an object of the present invention to provide an improvement design for digital servo motor controller IC for preventing the power feedback effect during manual adjusting the servo motor. A circuit is added respectively at the original outputs of the digital servo motor controller, which comprises a divider
10 and three NMOS inverters connected sequentially. The outputs of the second and the third NMOS are used as the inputs of the servo motor, so as to achieve preventing the power feedback effect during manual adjusting the servo motor. When the system is not powered on, the motor can be rotated manually to adjust the initial
15 position very easily.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 shows schematically the system block diagram of a conventional remote-controlled game motor or plane.

20 Fig. 2 shows schematically the conventional circuit diagram between a digital servo motor controller IC and a motor.

Fig. 3 shows schematically the circuit added in a digital servo motor controller IC according to the present invention.

25 DETAILED DESCRIPTION OF THE INVENTION

Referring to Fig. 3, which shows schematically the circuit added in a digital servo motor controller IC according to the

present invention. The circuit is added after each of the two outputs OUT1 and OUT2 of the original digital servo motor controller 4 in IC.

5 The output OUT1 will be used as an example for explanation as shown in Fig. 3. Resistors R1 and R2 form a divider, OUT1 is sent to the input of R1, the connecting point "a" between R1 and R2 is connected with the gate of NMOS5. The drain of NMOS5 is connected at point "b" with the gate of NMOS6. The drain of NMOS6 is connected at point "c" with the gate of NMOS7. The point "c" is the output OUT11. The point "d" is the drain of NMOS7, and is the output OUT11B. OUT11 and OUT11B are used to substitute the OUT1 and OUT1B in Fig. 2 for driving the motor 5 according to the present invention.

15 Similarly, as to output OUT2, the present invention will use the similar circuit as in Fig. 3 to generate OUT22 and OUT22B to substitute the OUT2 and OUT2B in Fig. 2 for driving the motor 5.

The resistances of the resistors R1 and R2 are properly designed such that when the voltage of OUT1 is higher than or equal to 3 volts, NMOS5 will be conducting; when the voltage of OUT1 is lower than 3 volts, NMOS5 will be opened.

20 When the system is not powered on, if we try to rotate the motor 5 manually so as to generate some little power (this is the most possible case) to be fed back to the system, and make OUT1 lower than 3 volts, than NMOS5 will be opened, the voltage at point "b" will be high, NMOS6 is therefore conducting. When NMOS6 is conducting, the voltage of point "c" will be low (i.e. OUT11 is "0"), NMOS7 is therefore opened, the voltage at point

"d" will be high. (i.e. OUT11B is "1").

Similarly, when the system is not powered on, if we try to rotate the motor 5 manually so as to generate some little power (this is the most possible case) to be fed back to the system, and make OUT2 lower than 3 volts, than NMOS5 will be opened, the voltage at point "b" will be high, NMOS6 is therefore conducting. When NMOS6 is conducting, the voltage of point "c" will be low (i.e. OUT22 is "0"), NMOS7 is therefore opened, the voltage at point "d" will be high. (i.e. OUT22B is "1").

Therefore, when the system is not powered on, if we try to rotate the motor 5 manually so as to generate some little power (this is the most possible case) to be fed back to the system, OUT11 will be "0", OUT11B will be "1", OUT22 will be "0", OUT22B will be "1", so the four power MOS transistors PMOS1, NMOS2, PMOS3, NMOS4 will all be opened. Consequently, there is no power feedback effect as to the motor 5, and we can rotate the motor 5 very easily to achieve the purpose of the present invention.

The circuit in Fig. 3 can also prevent the motor from trembling (power exhausting) during normal power-on (not feed back power) as OUT1, OUT2 to be "0, 1", "1, 0" or "1, 1"

When the system is not powered on, if we try to rotate the motor 5 manually so as to generate some big power (this is not the normal possible case) to be fed back to the system, the IC of digital servo motor controller 4 will work as being powered on, and OUT1, OUT2 will be controlled by the signal from the joystick 1 of the remote-controlled signal transmitter 2. However, since

the joystick 1 does not send out any signal, OUT1 and OUT2 will be both "0", so OUT11 and OUT22 will be both "0". Referring to table 1, we know that the motor 5 will not rotate, i.e. no power (voltage) is passed through the motor 5. Therefore it is very easy
5 to rotate the motor 5 to achieve the purpose of the present invention.

The circuit in Fig. 3 of the present invention is built in the IC of digital servo motor controller 4. PMOS can be used instead to achieve the purpose of the present invention.

10 The spirit and scope of the present invention depend only upon the following Claims, and are not limited by the above embodiments.

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